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Stephen Brown, Zvonko Vranesic. Fundamentals of Digital Logic With Verilog Design is intended for an introductory course in digital logic design. The main goals are (1) to teach students the fundamental concepts in classical manual digital design, and (2) illustrate clearly the way in which digital circuits are designed today, using CAD tools. Use of CAD software is well integrated into the book.

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Fundamentals of Digital Logic With Verilog Design is intended for an introductory course in digital logic design.

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Fundamentals of digital logic with Verilog design / Stephen Brown and Zvonko Vranesic. — Third edition. pages cm ISBN 978-0-07-338054-4 (alk. paper) 1. Logic circuits—Design and construction—Data processing. 2.

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Fundamentals of Digital Logic with Verilog Design, by Brown & Vranesic, 2nd ed (2012), ISBN 978-0-07-066724-2 This is described as a 2012 edition but is actually an incomplete version of the 2008 edition. (It is in fact the 2012 Special Indian Edition.)

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"Fundamentals of Digital Logic With VHDL Design" teaches the basic design

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